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L1	503	708/300.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 20:27
L2	1980	malik.in. or goel.in. and latency	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 20:28
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L4	1	(malik.in. or goel.in.) and latency and "708"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 20:28
L5	2	(malik.in. and goel.in.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 20:30
L6	23290	"708"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 20:31
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L8	2595	708/300-323.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 20:33
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L10	53	9 and (sequential or serial)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 20:34
L11	16	(carry-out or (carry adj out)) and 10	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 20:37
L12	15	(carry-out or (carry adj out)) and previous and 9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/05 20:37
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Volume 43, Issue 7, July 1996 Page(s):529 - 534

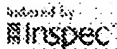
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2. A systolic architecture for LMS adaptive filtering with minimal adaptation delay

Ramanathan, S.; Visvanathan, V.;

VLSI Design, 1996. Proceedings., Ninth International Conference on

3-6 Jan. 1996 Page(s):286 - 289

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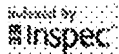
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